

ABSTRACT

A video signal processing circuit that uses a prescribed clock signal to process a digitized composite video signal. A clock generating means (2) generates the prescribed clock signal; a burst phase detecting means (3) detects color subcarrier phase information (p) in each line of the composite video signal; a phase difference calculation means (4) finds the phase difference between phase information (p) from the burst phase detecting means and a prescribed reference phase; a sampling phase conversion means (8) corrects the sampling phase of the composite video signal according to phase corrections (Δb , Δt) obtained from the phase difference calculation means (4); a Y/C separation means (9) separates the luminance and chrominance signals from the composite video signal output from the sampling phase conversion means (8). Excellent two- or three-dimensional Y/C separation can be obtained regardless of the television broadcast system, even from a non-standard signal.